IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No				0/071,	425	
Priority Filing Date			Februa	ry 8, 2	002	
Inventor			Terry L. G	ilton e	t al.	
Assignee			Micron Techno	ology,	Inc.	
Priority Group Art Unit				2	813	
Priority Examiner			Thanhha	a S. Pr	nam	
Attorney Docket No		MI22-2440				
Customer No				021	567	
Title: Semiconductor Processing	Method	Using	Photoresist	and	an	
Antireflective Coating						

INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 10/071,425, filed February 8, 2002. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 11-21-63

Mark S. Matkin, Reg. No. 32,268

Form PTO-1449

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. MI22-2440

PRIORITY SERIAL NO.: 10/071,425

LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)

APPLICANT: Terry L. Gilton et al.

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U.S. PATE	NT DOC	UMENTS							
*Examiner's Initials		Document Number	Date	Name		Class	Subclass	Filing D)ate oriate
	A	4,127,414	11/28/78	Yoshikawa et al.			_		
	AB	4,320,191	03/16/82	Yoshikawa et al.					
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1	AJ.	60-226123	11.11.85	Japan (Toshiba Corp.)	· · · · · · · ·			Yes Abstract	No
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